CPE301 – SPRING 2019

Design Assignment DA1B

Student Name: Carlos D. Zuniga

Student #: 2000783713

Student Email: zunigc2@unlv.nevada.edu

Primary Github address: https:

Directory:

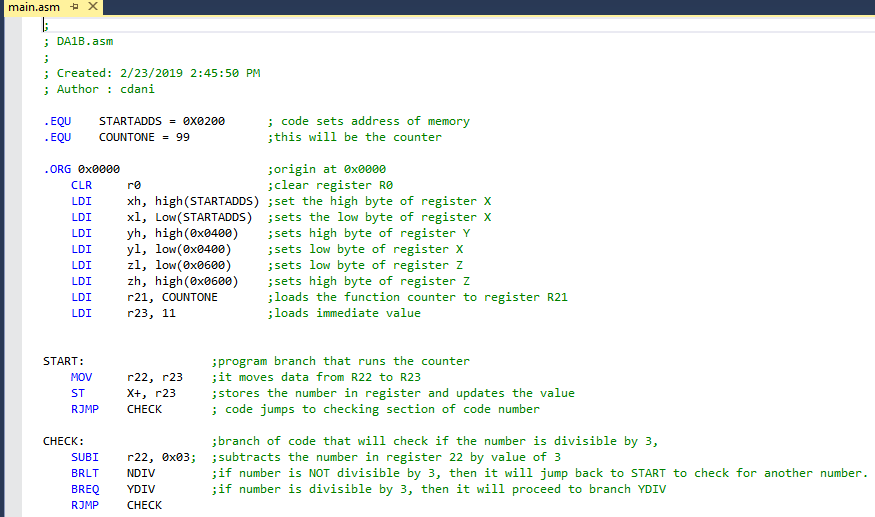
Submit the following for all Labs:

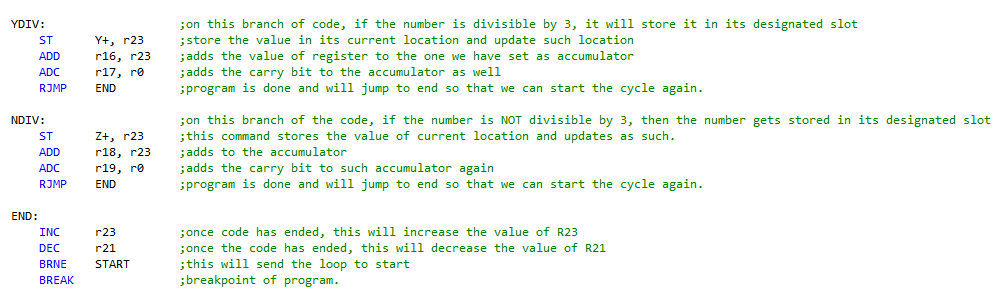
1. In the document, for each task submit the modified or included code (only) with highlights and justifications of the modifications. Also, include the comments.
2. Use the previously create a Github repository with a random name (no CPE/301, Lastname, Firstname). Place all labs under the root folder ESD301/DA, sub-folder named LABXX, with one document and one video link file for each lab, place modified asm/c files named as LabXX-TYY.asm/c.
3. If multiple asm/c files or other libraries are used, create a folder LabXX-TYY and place these files inside the folder.
4. The folder should have a) Word document (see template), b) source code file(s) and other include files, c) text file with youtube video links (see template).

1. **COMPONENTS LIST AND CONNECTION BLOCK DIAGRAM w/ PINS**

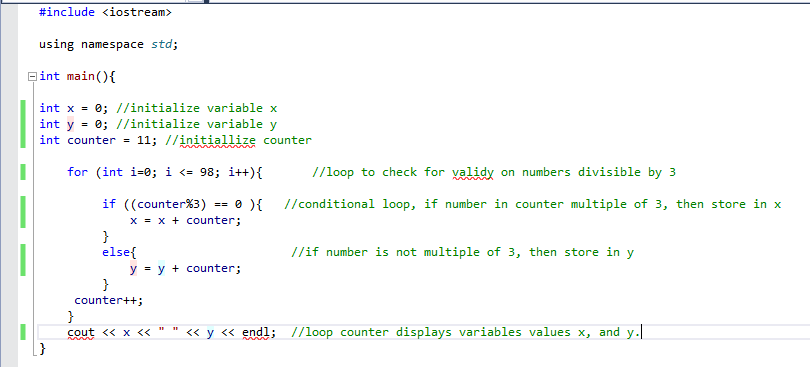
Atmel Studio 7

1. **Complete code of assignments with comments for all three tasks**

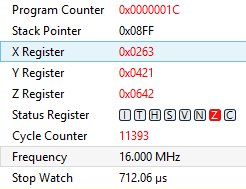


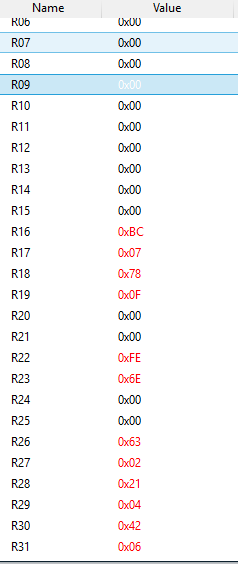


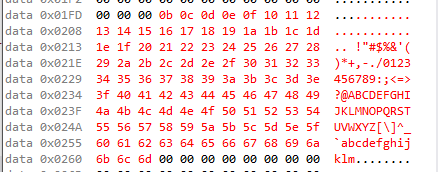
1. **Verification with C coding**

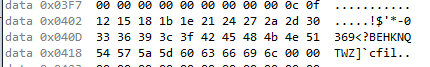


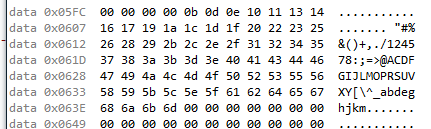
1. **SCREENSHOTS OF EACH TASK OUTPUT (ATMEL STUDIO OUTPUT)**

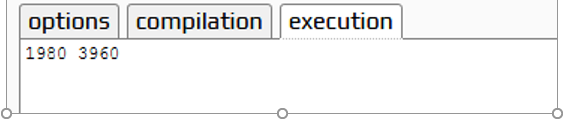












1. **SCREENSHOT OF EACH DEMO (BOARD SETUP)**

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1. **VIDEO LINKS OF EACH DEMO**
2. **GITHUB LINK**

**Student Academic Misconduct Policy**

<http://studentconduct.unlv.edu/misconduct/policy.html>

“This assignment submission is my own, original work”.

NAME OF THE STUDENT